

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S27	1	@ad<"20020719" and (exclud\$3 exclusion omit\$4 ignor\$3 disregard\$3 discount\$3 neglect\$3) with ("near" surrounding neighbor\$3 vicinity nearby close proximit\$3) with (transistor "mos" "cmos" mosfet) with (drc (rule near2 check\$3) verif\$7 check\$3 validat\$3 test\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/15 15:16
S26	120	@ad<"20020719" and (exclud\$3 exclusion omit\$4 ignor\$3 disregard\$3 discount\$3 neglect\$3) with ("near" surrounding neighbor\$3 vicinity nearby close proximit\$3) with (transistor "mos" "cmos" mosfet)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/15 15:17
S29	26	@ad<"20020719" and (exclud\$3 exclusion omit\$4 ignor\$3 disregard\$3 discount\$3 neglect\$3) with ("near" surrounding neighbor\$3 vicinity nearby close proximit\$3) with (transistor "mos" "cmos" mosfet) and (drc (rule near2 check\$3) verif\$7 check\$3 validat\$3 test\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/15 15:20
S28	2	@ad<"20020719" and (exclud\$3 exclusion omit\$4 ignor\$3 disregard\$3 discount\$3 neglect\$3) with ("near" surrounding neighbor\$3 vicinity nearby close proximit\$3) with (transistor "mos" "cmos" mosfet) same (drc (rule near2 check\$3) verif\$7 check\$3 validat\$3 test\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/15 15:23
S30	50	@ad<"20020719" and (exclud\$3 exclusion omit\$4 ignor\$3 disregard\$3 discount\$3 neglect\$3) same ("near" surrounding neighbor\$3 vicinity nearby close proximit\$3) same (transistor "mos" "cmos" mosfet) same (drc (rule near2 check\$3) verif\$7 check\$3 validat\$3 test\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/15 15:26
S31	1	"20040163060" and transistor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/15 15:27

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L1	394	@ad<"20020719" and (test\$3 check\$3 verif\$7 drc ((design rule) with check\$3)) with (transistor mos cmos pmos nmos) with ("near" surrounding neighbor\$3 vicinit\$3 local close proximitt\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/17 11:56
L3	534	@ad<"20020719" and (test\$3 check\$3 verif\$7 drc ((design rule) with check\$3)) same (transistor mos cmos pmos nmos) with (exclud\$3 exclusion\$3 omit\$4 ignor\$3 disregard\$3 discount\$3 avoid\$4 neglect\$3 forget\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/17 11:59
L2	11	@ad<"20020719" and (test\$3 check\$3 verif\$7 drc ((design rule) with check\$3)) with (transistor mos cmos pmos nmos) with ("near" surrounding neighbor\$3 vicinit\$3 local close proximitt\$3) same (exclud\$3 exclusion\$3 omit\$4 ignor\$3 disregard\$3 discount\$3 avoid\$4 neglect\$3 forget\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/17 11:59
L4	193	@ad<"20020719" and (test\$3 check\$3 verif\$7 drc ((design rule) with check\$3)) with (transistor mos cmos pmos nmos) with (exclud\$3 exclusion\$3 omit\$4 ignor\$3 disregard\$3 discount\$3 avoid\$4 neglect\$3 forget\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/17 12:01
L7	48	l6 and "716"/\$2.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/17 12:14
L5	77	@ad<"20020719" and (test\$3 check\$3 verif\$7 drc ((design rule) with check\$3)) with (transistor mos cmos pmos nmos) near5 (exclud\$3 exclusion\$3 omit\$4 ignor\$3 disregard\$3 discount\$3 avoid\$4 neglect\$3 forget\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/17 12:14
L6	2640	@ad<"20020719" and (test\$3 check\$3 verif\$7 drc ((design rule) with check\$3)) and (transistor mos cmos pmos nmos) near5 (exclud\$3 exclusion\$3 omit\$4 ignor\$3 disregard\$3 discount\$3 avoid\$4 neglect\$3 forget\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/17 12:15

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L8	22	@ad<"20020719" and (test\$3 check\$3 verif\$7 drc ((design rule) with check\$3)) and (transistor mos cmos pmos nmos) near5 (exclud\$3 exclusion\$3 ignor\$3 disregard\$3 discount\$3 neglect\$3 forget\$3) and "716"/\$2.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/17 12:39
L10	38390	@ad<"20020719" and (wire wiring line conductor) with (power ground vdd vss gnd) with transistor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/17 12:40
L9	9251	@ad<"20020719" and (wire wiring line conductor) with (power ground vdd vss gnd) with (width)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/17 12:40
L11	563	@ad<"20020719" and (wire wiring line conductor) with (power ground vdd vss gnd) with transistor with width	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/17 12:41
L12	124	@ad<"20020719" and width near3 (wire wiring line conductor) with (power ground vdd vss gnd) with transistor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/17 12:42
L13	61	@ad<"20020719" and (carry carries carrying supply\$3 supplies provide) with (width) near3 (wire wiring line conductor) with (power ground vdd vss gnd) with transistor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/17 12:44
L15	23165	@ad<"20020719" and (carry carries carrying connect\$3 interconnect\$3 provide) with (wire wiring line conductor) with (power ground vdd vss gnd) with transistor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/17 12:46
L14	49	@ad<"20020719" and (carry carries carrying connect\$3 interconnect\$3 provide) with (width) near3 (wire wiring line conductor) with (power ground vdd vss gnd) with transistor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/17 12:46

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L16	14443	@ad<"20020719" and (carry carries carrying connect\$3 interconnect\$3 provide) near5 (wire wiring line conductor) near5 (power ground vdd vss gnd) with transistor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/17 12:47
L17	12405	@ad<"20020719" and (carry carries carrying connect\$3 interconnect\$3 provide) near5 (wire wiring line conductor) near5 (power ground vdd vss gnd) near7 transistor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/17 12:52
L18	7381	@ad<"20020719" and (carry carries carrying connect\$3 interconnect\$3 provide) near5 (wire wiring line conductor) near (power ground vdd vss gnd) near5 transistor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/17 12:53